

FIG. 5a

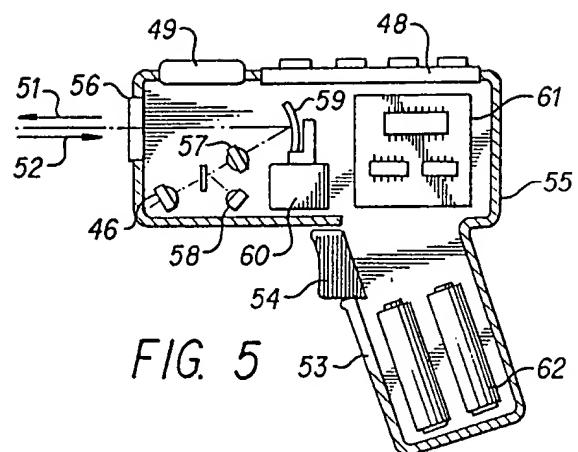


FIG. 5

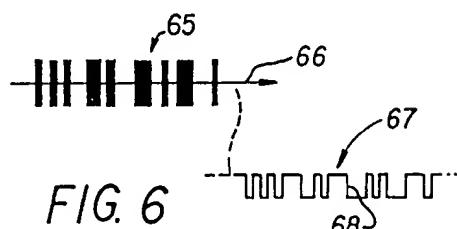


FIG. 6

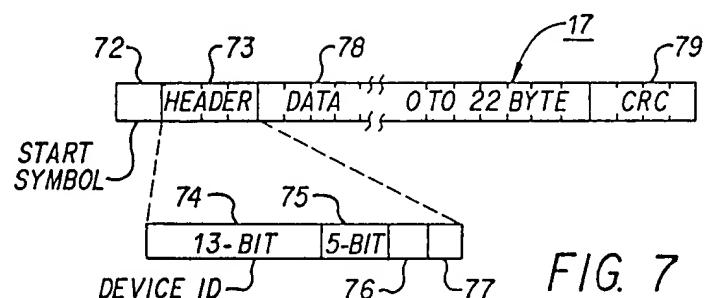


FIG. 7

FIG. 8a



CHIP 80 CHIP 80 CHIP 80
 $1.5\mu\text{SEC}$ $1.5\mu\text{SEC}$ 0.667 MHZ
 CHIP RATE

FIG. 8b

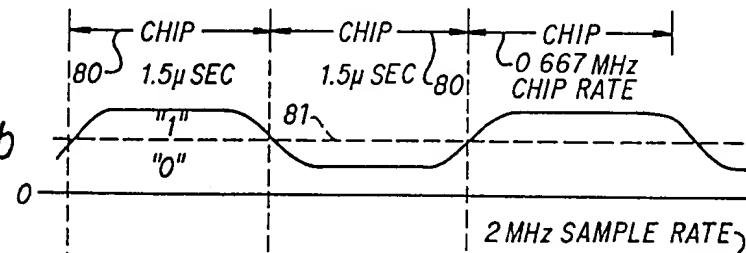


FIG. 8c



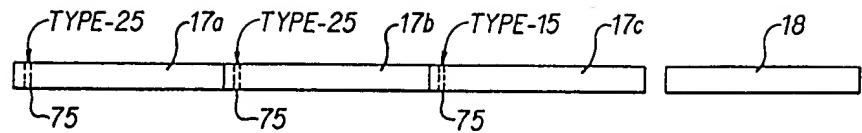


FIG. 7a

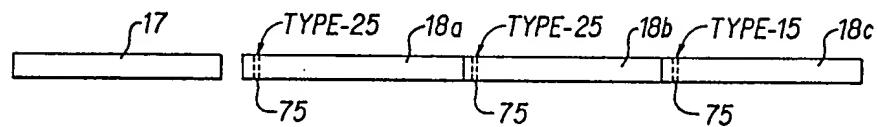


FIG. 7b

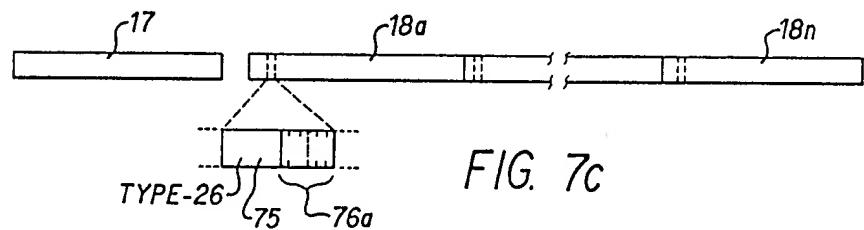


FIG. 7c

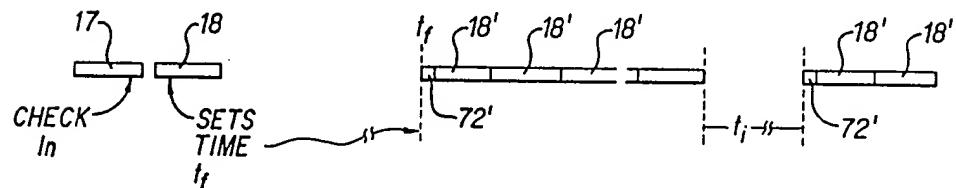


FIG. 7d

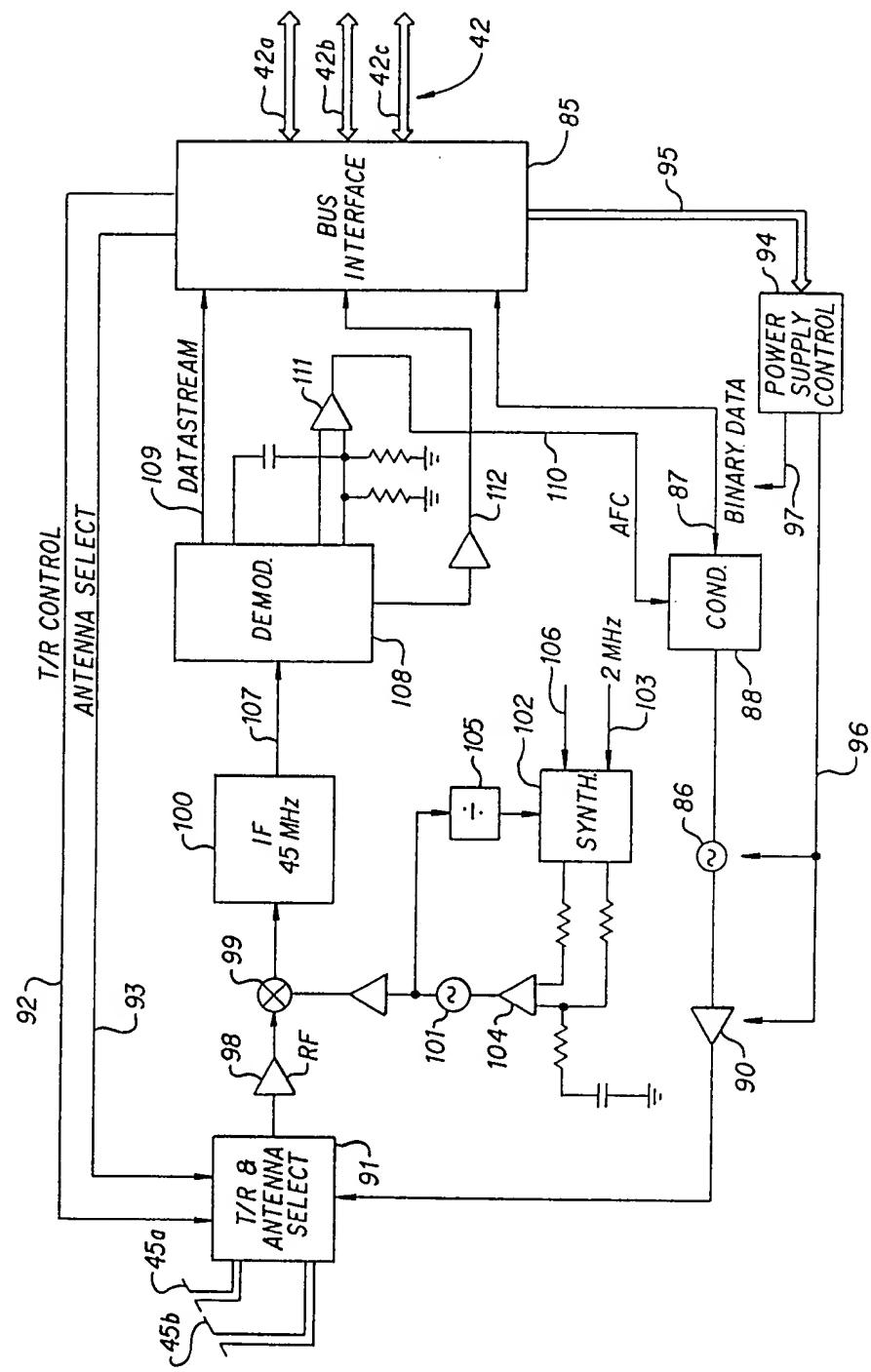
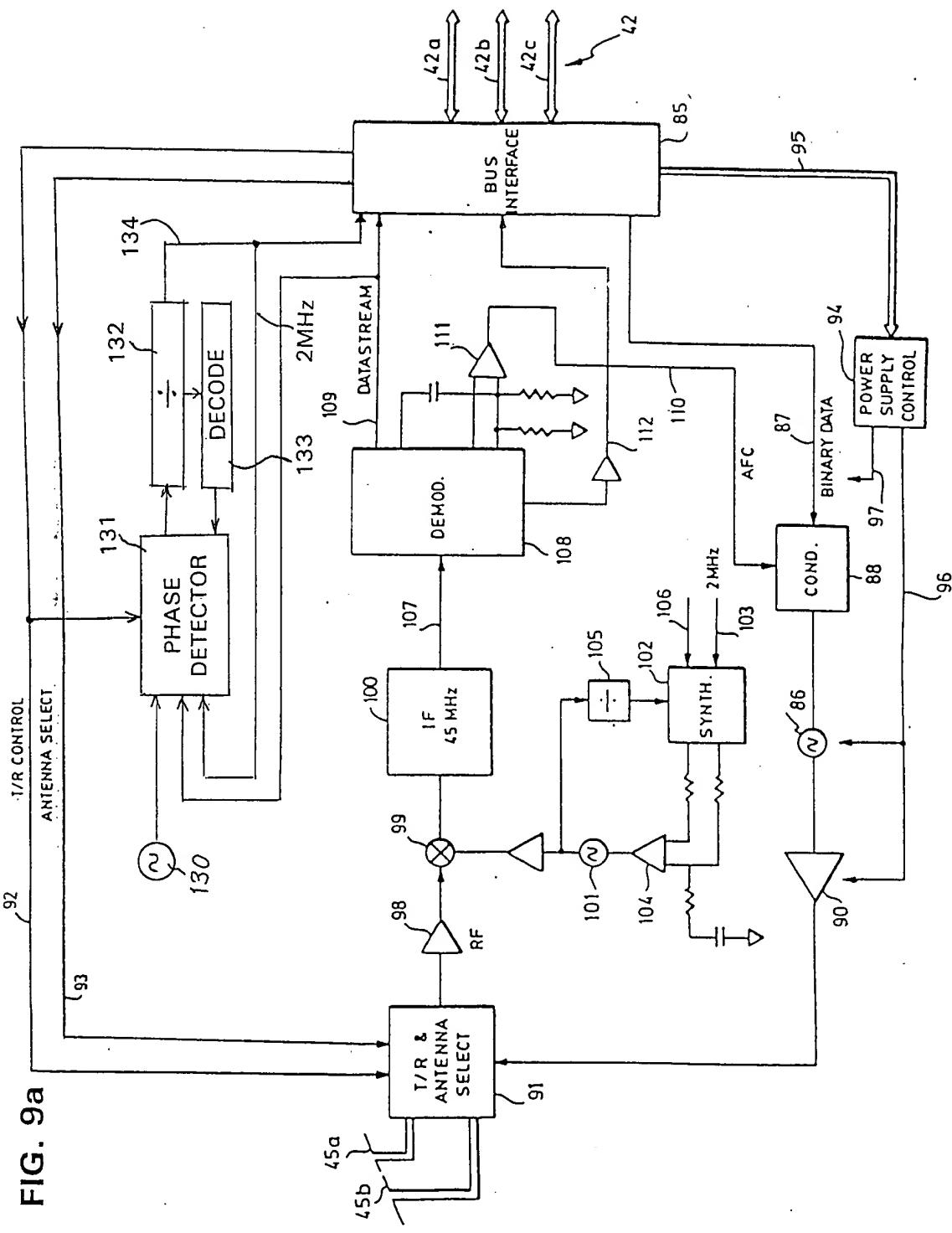


FIG. 9



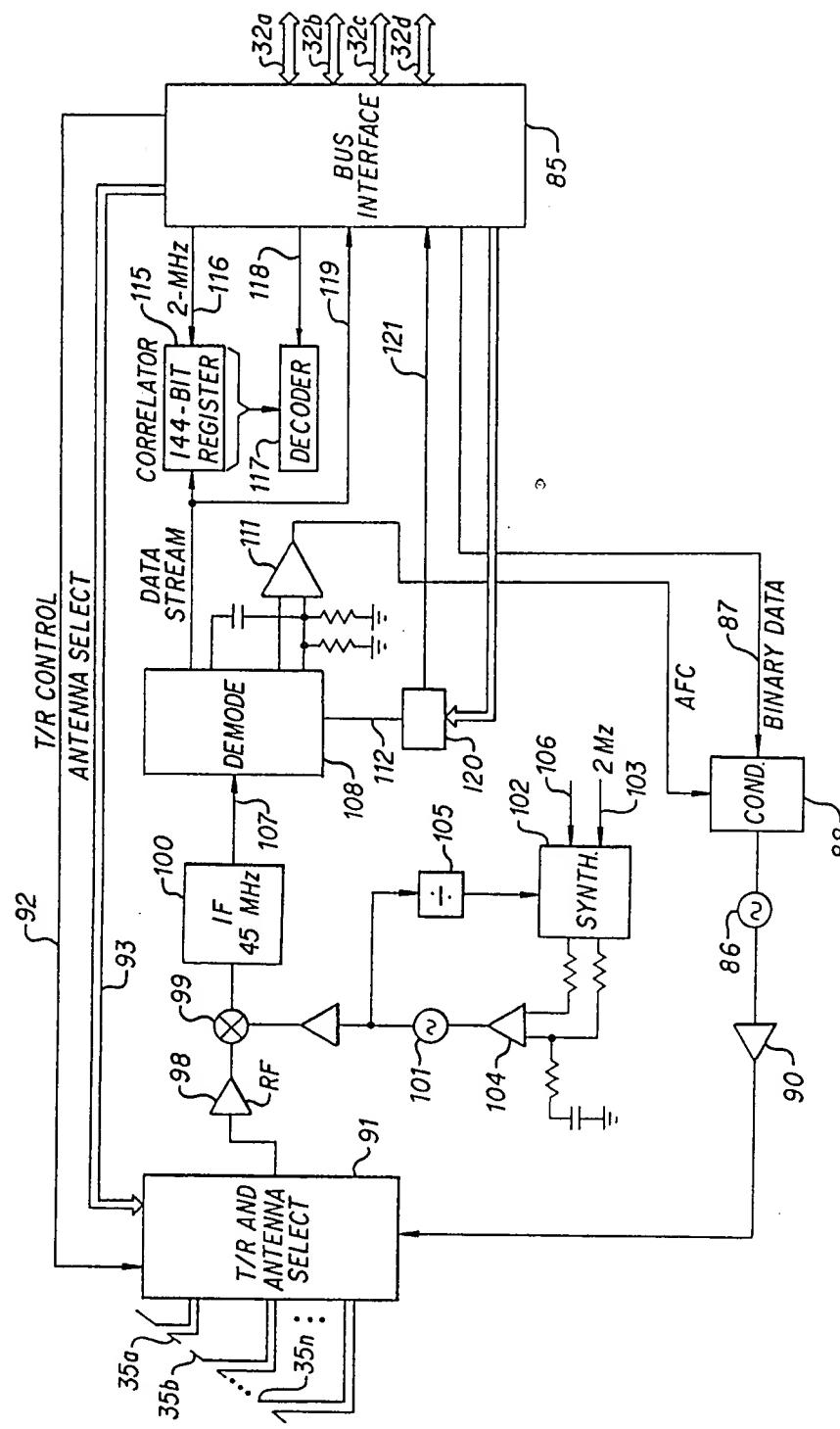


FIG. 10

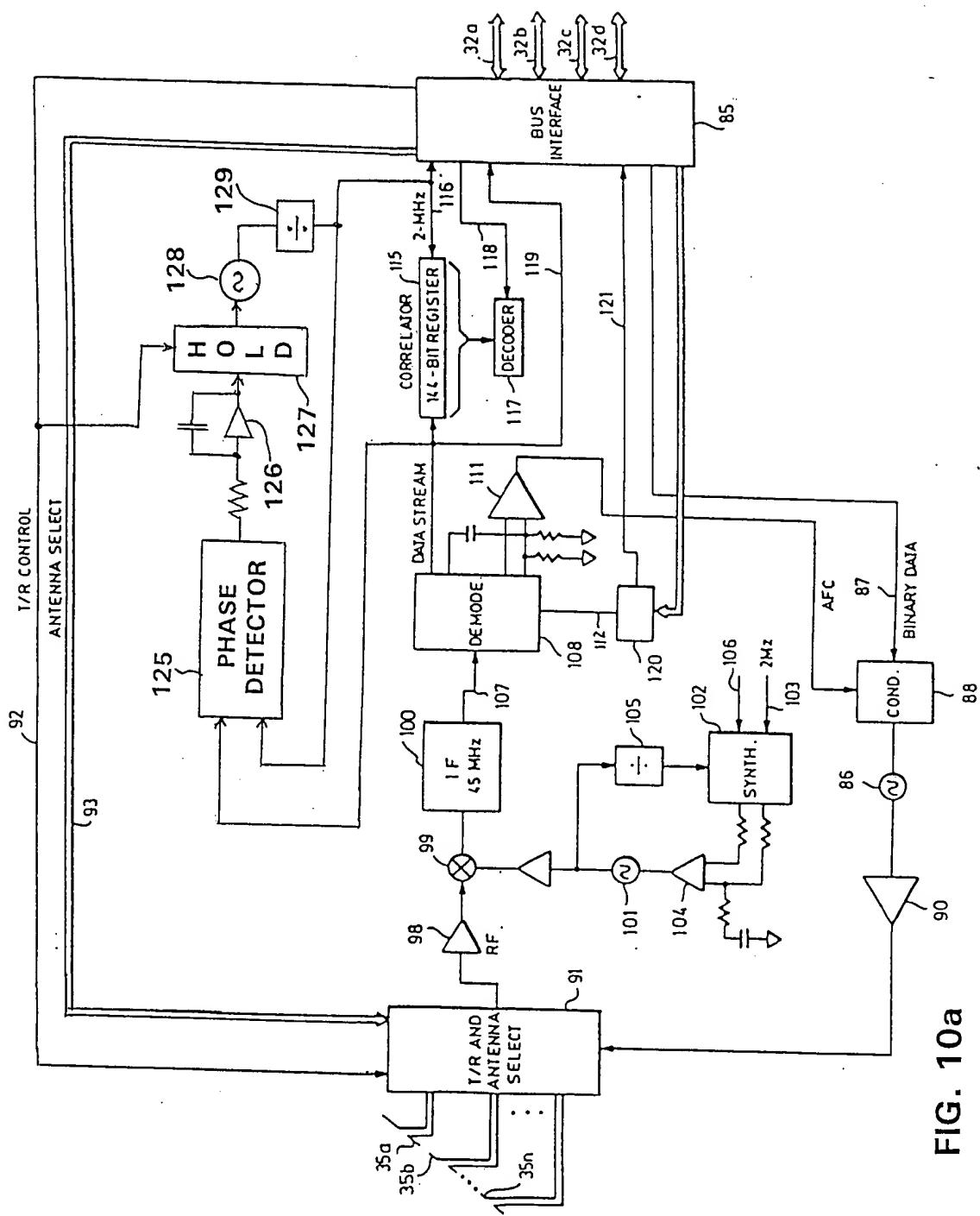


FIG. 10a

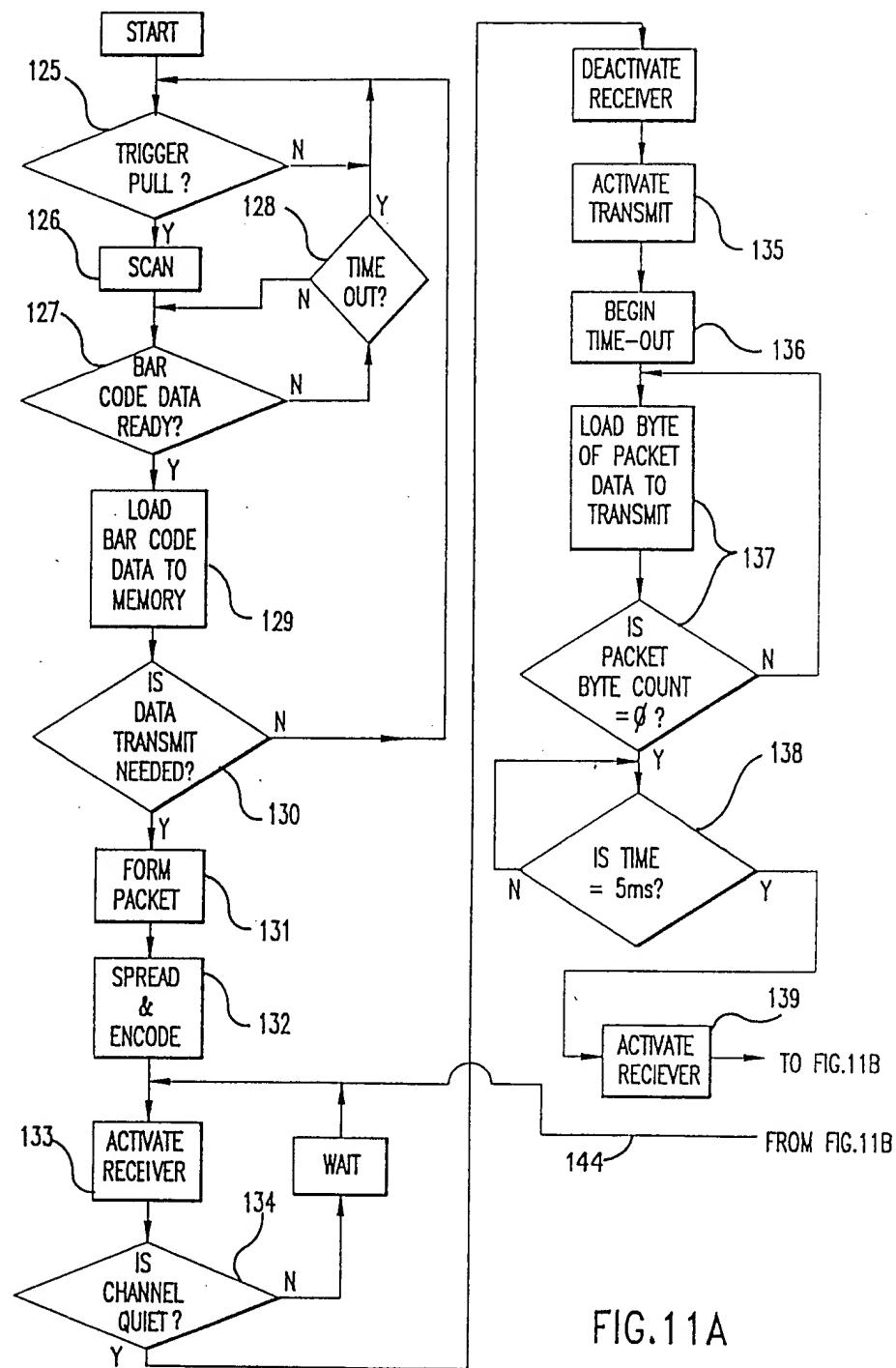


FIG.11A

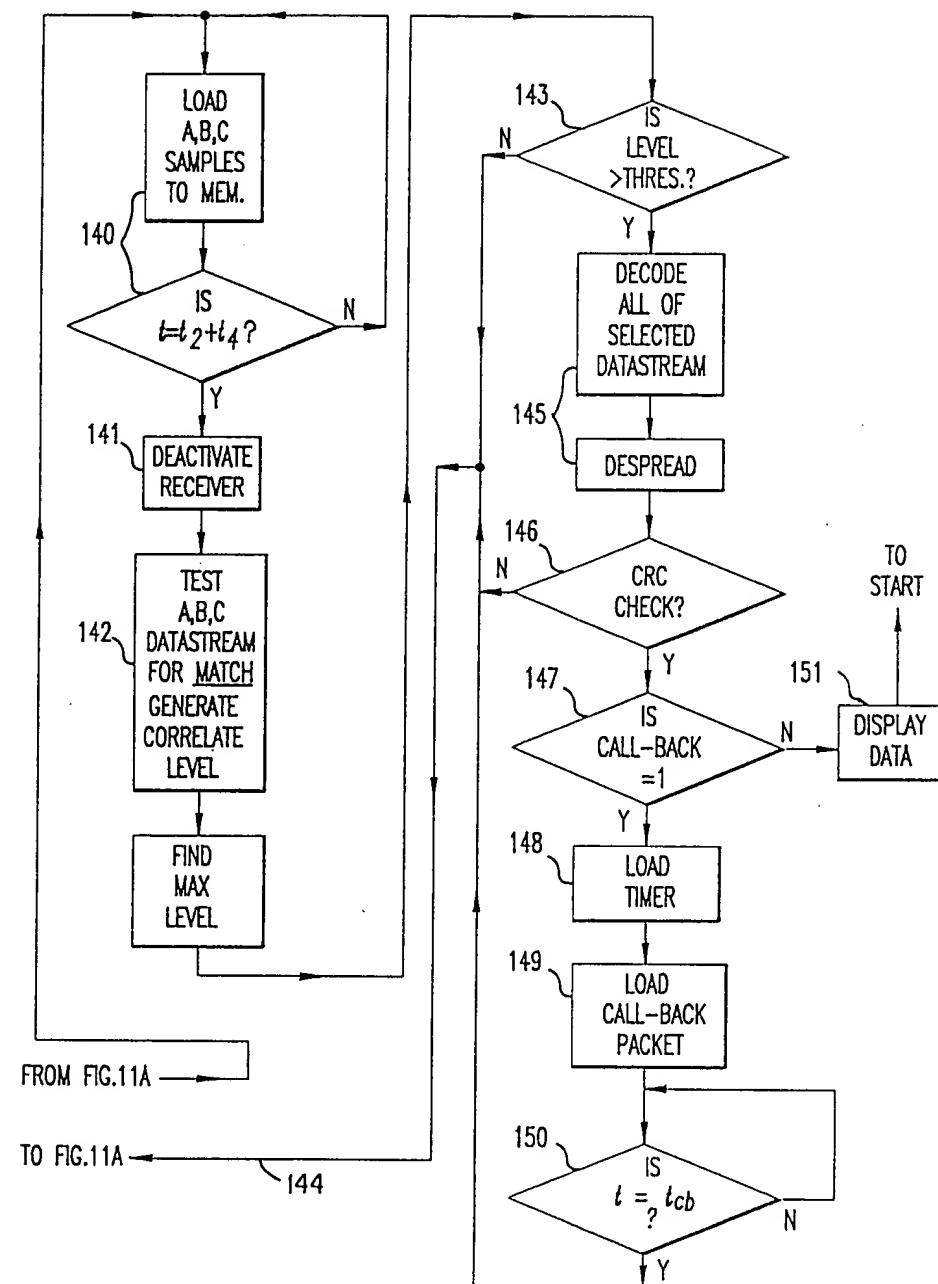


FIG.11B

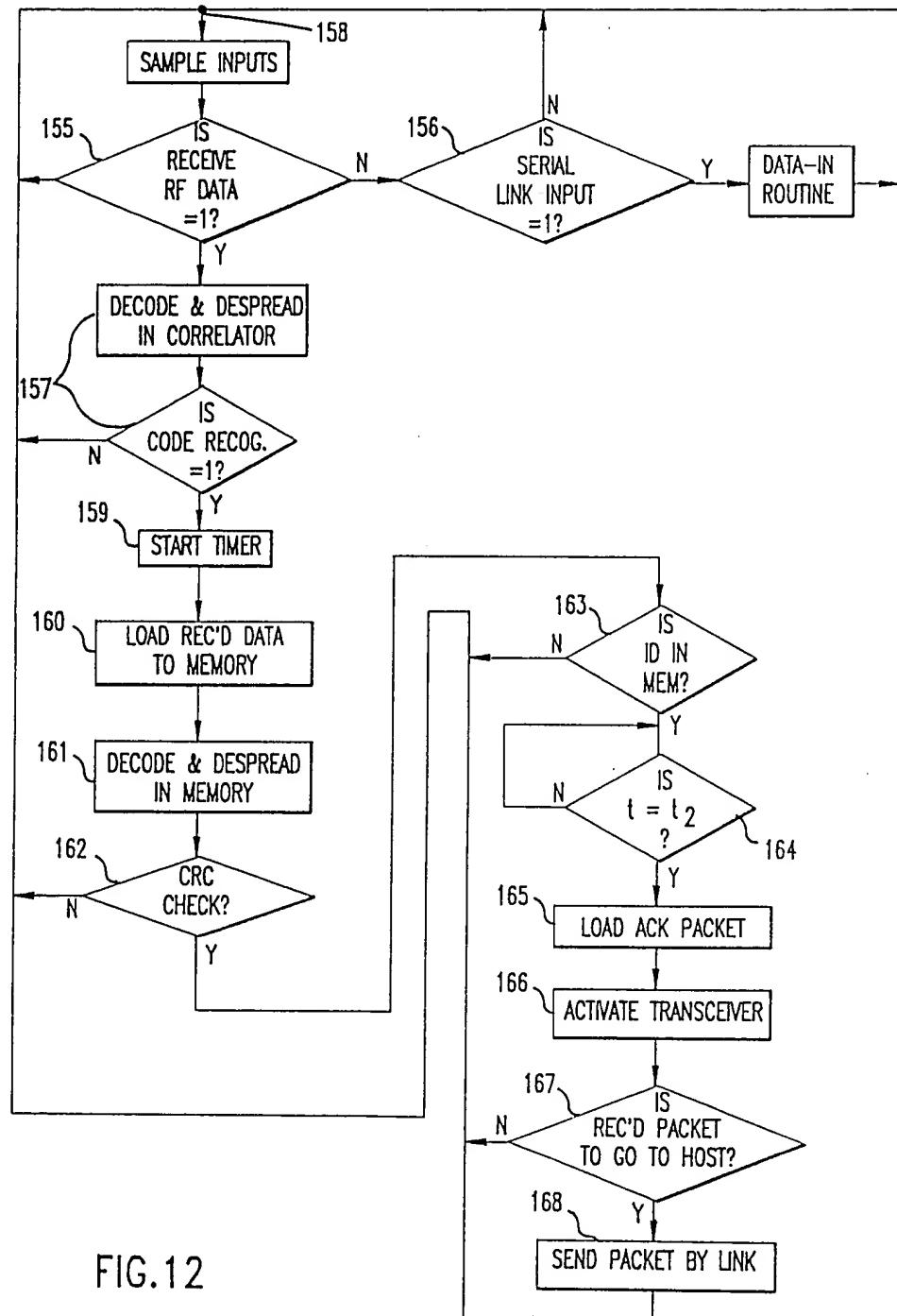


FIG.12

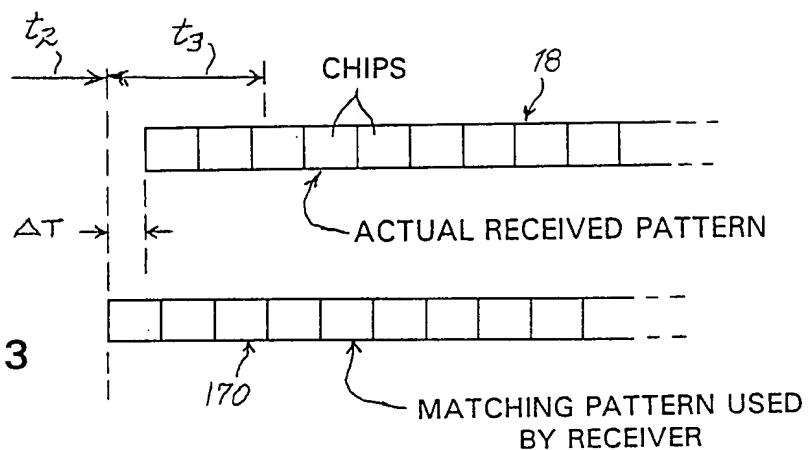


FIG. 13

CORRECTED START TIME

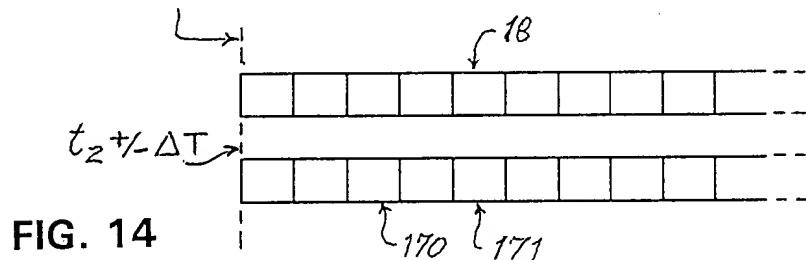


FIG. 14

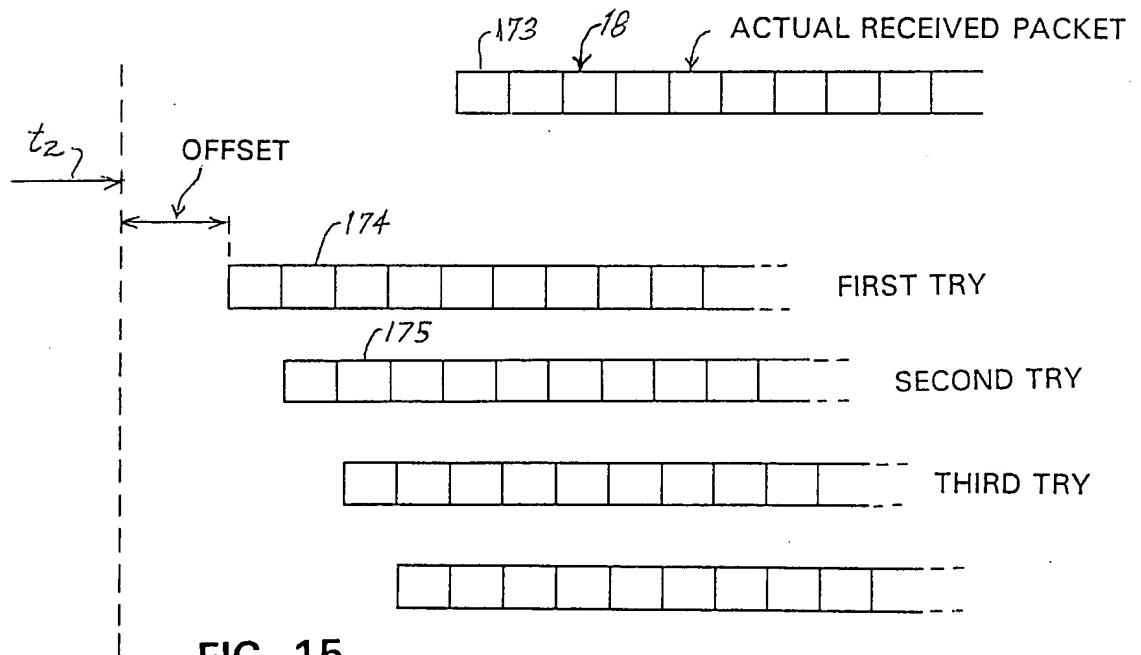


FIG. 15